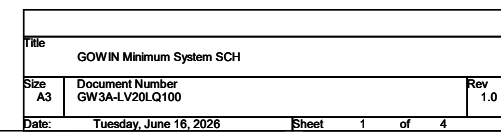
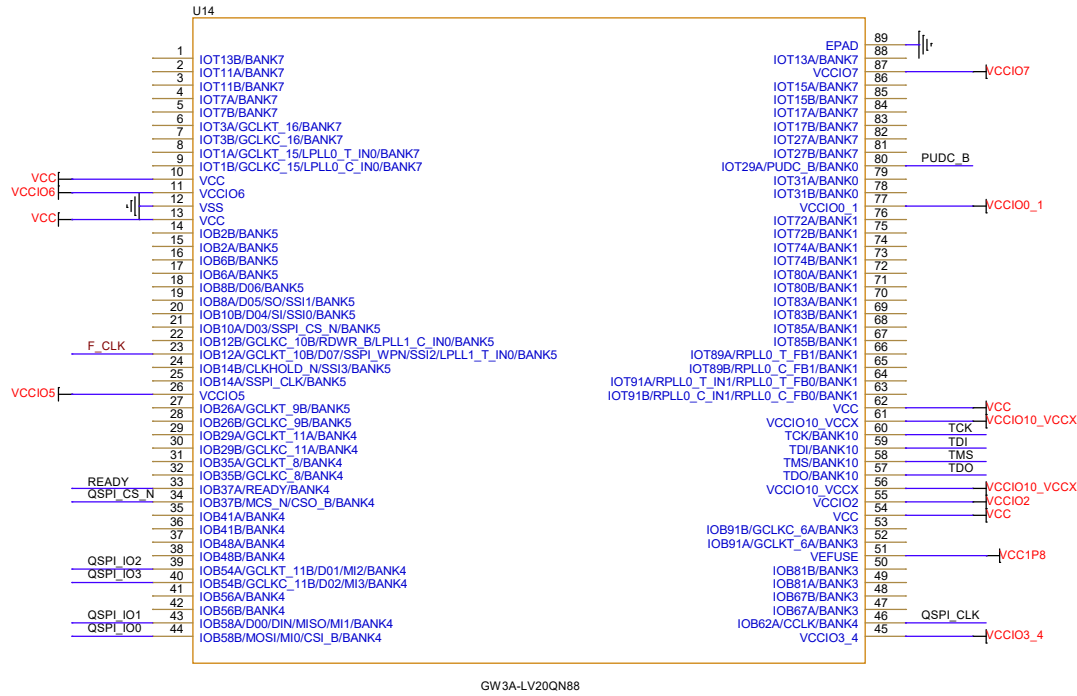


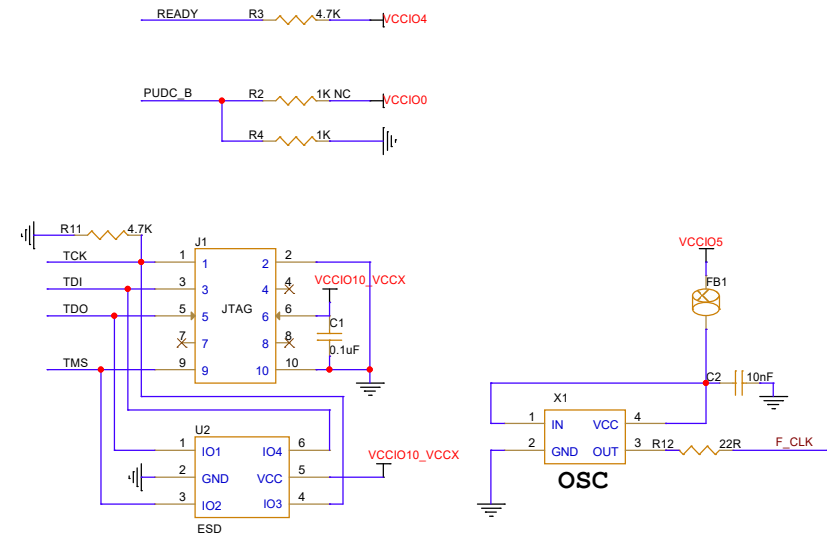
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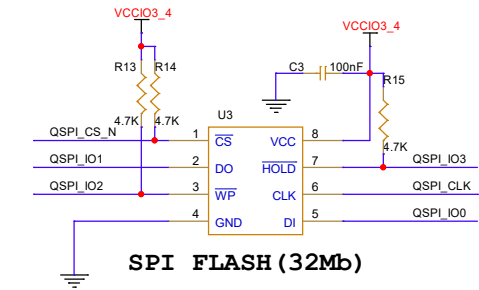
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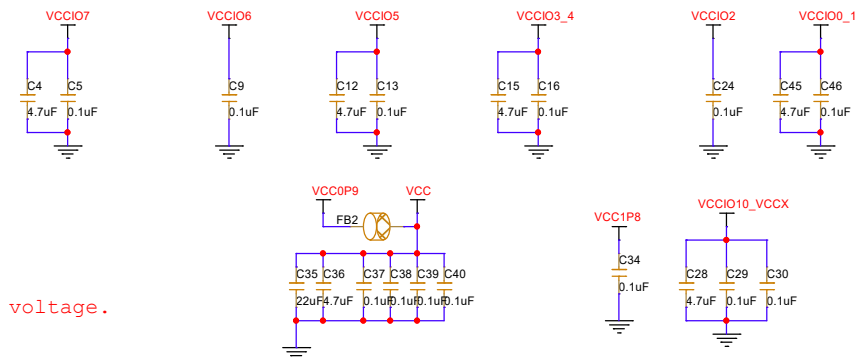
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*JTAG download section



SPI FLASH (32Mb)

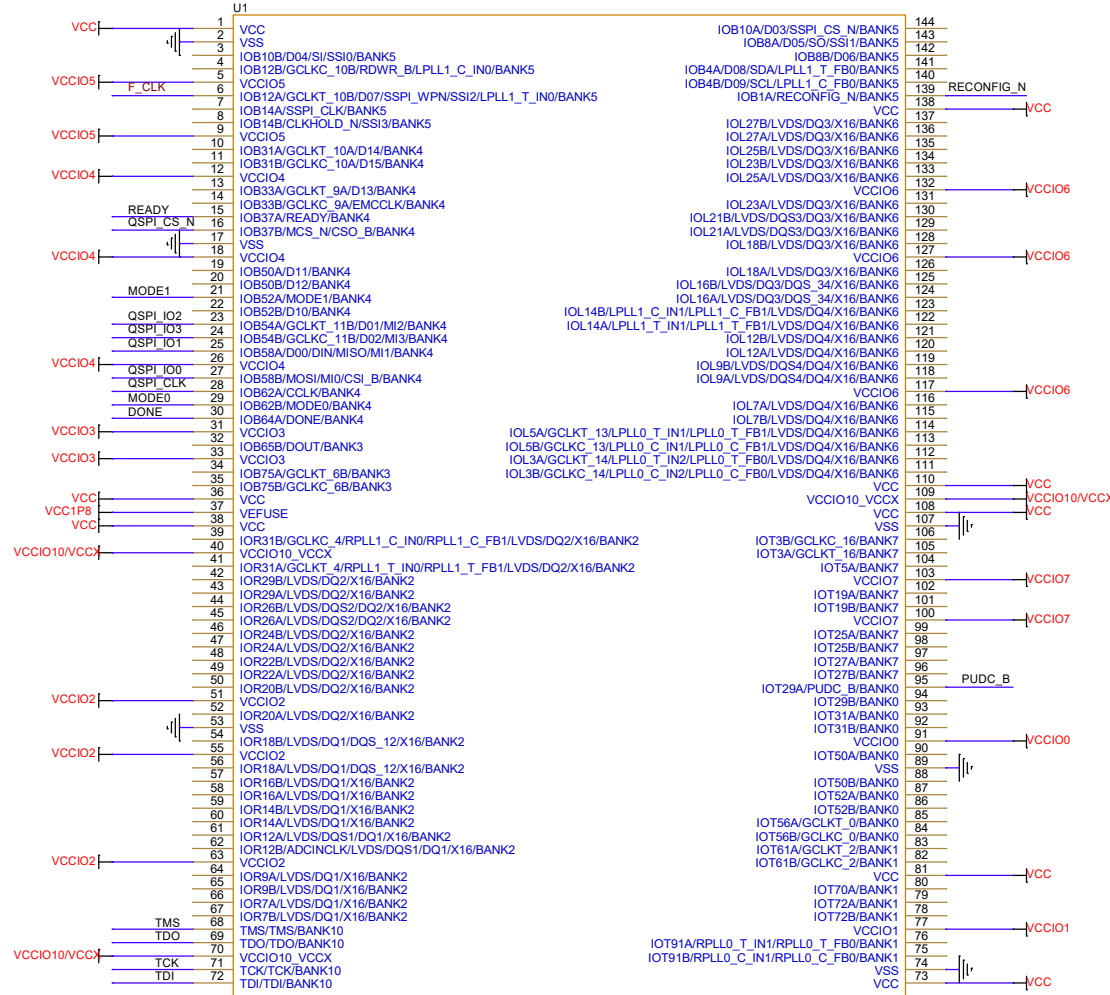


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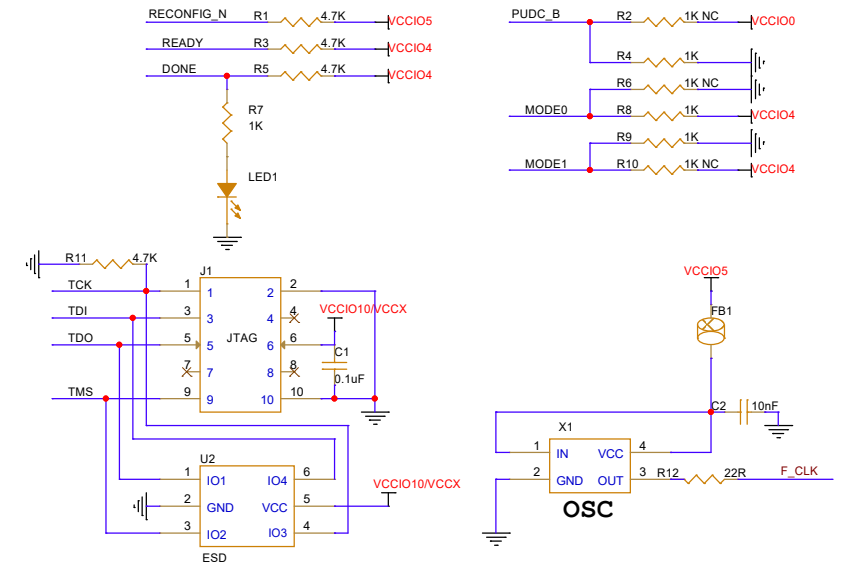
- 1、 It is recommended that add an ESD protection chip to the JTAG download circuit.
- 2、 The VCCX value must be set in the GOWIN EDA tool to be consistent with the actual supply voltage.
- 3、 When VEFUSE is not needed, this power supply can be connected to GND or left floating.
- 4、 PUDC_B=0: All GPIOs except PUDC_B have weak pull-up
; PUDC_B=1: All GPIOs have high impedance

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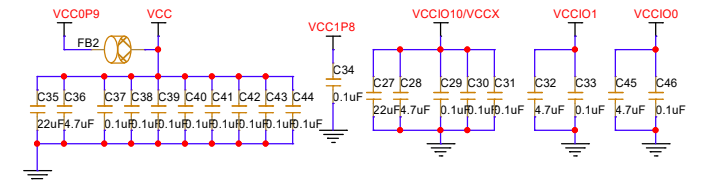
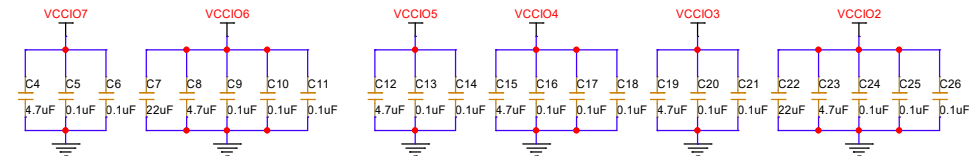
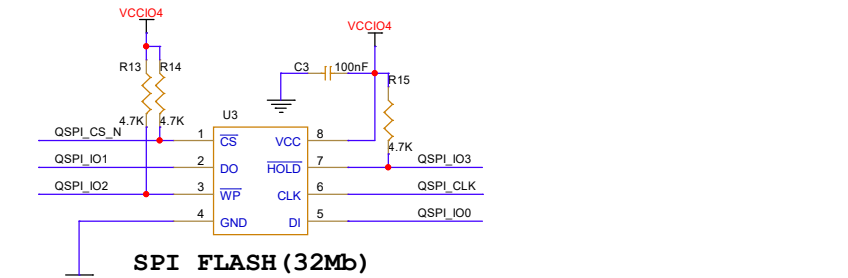
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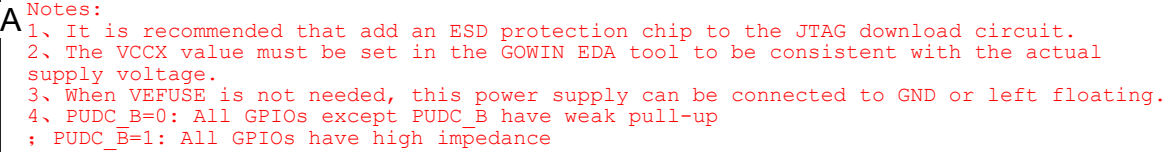
*JTAG download section



- Notes:
- 1、It is recommended that add an ESD protection chip to the JTAG download circuit.
 - 2、The VCCX value must be set in the GOWIN EDA tool to be consistent with the actual supply voltage.
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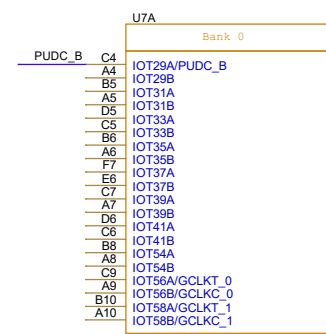
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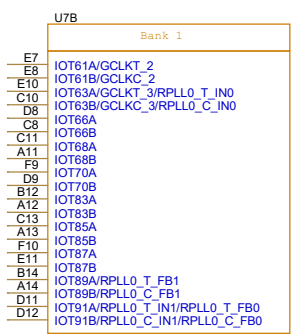
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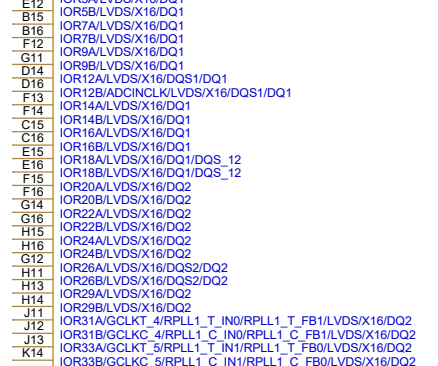
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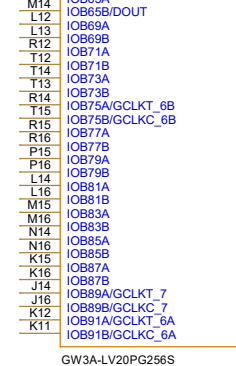
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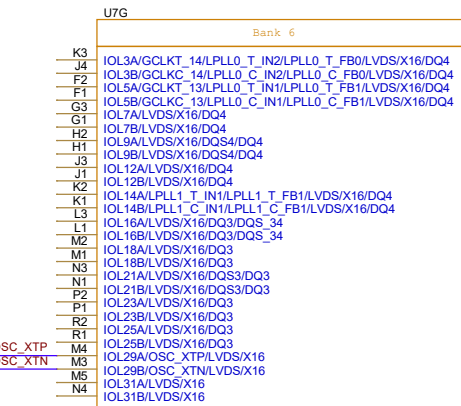
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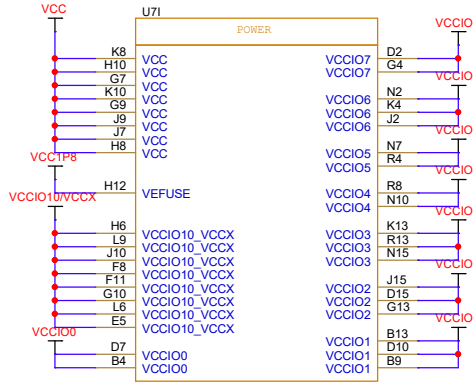
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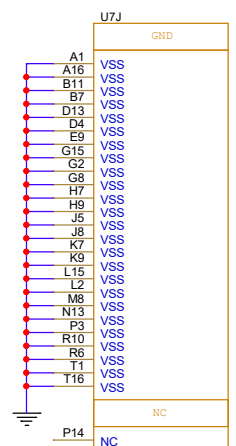
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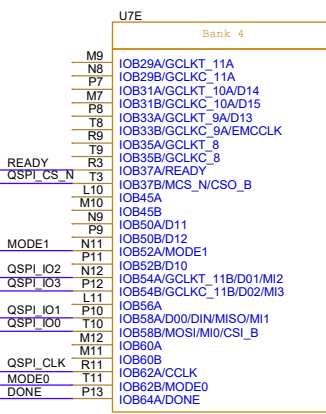
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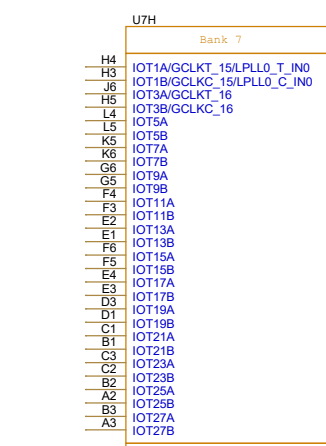
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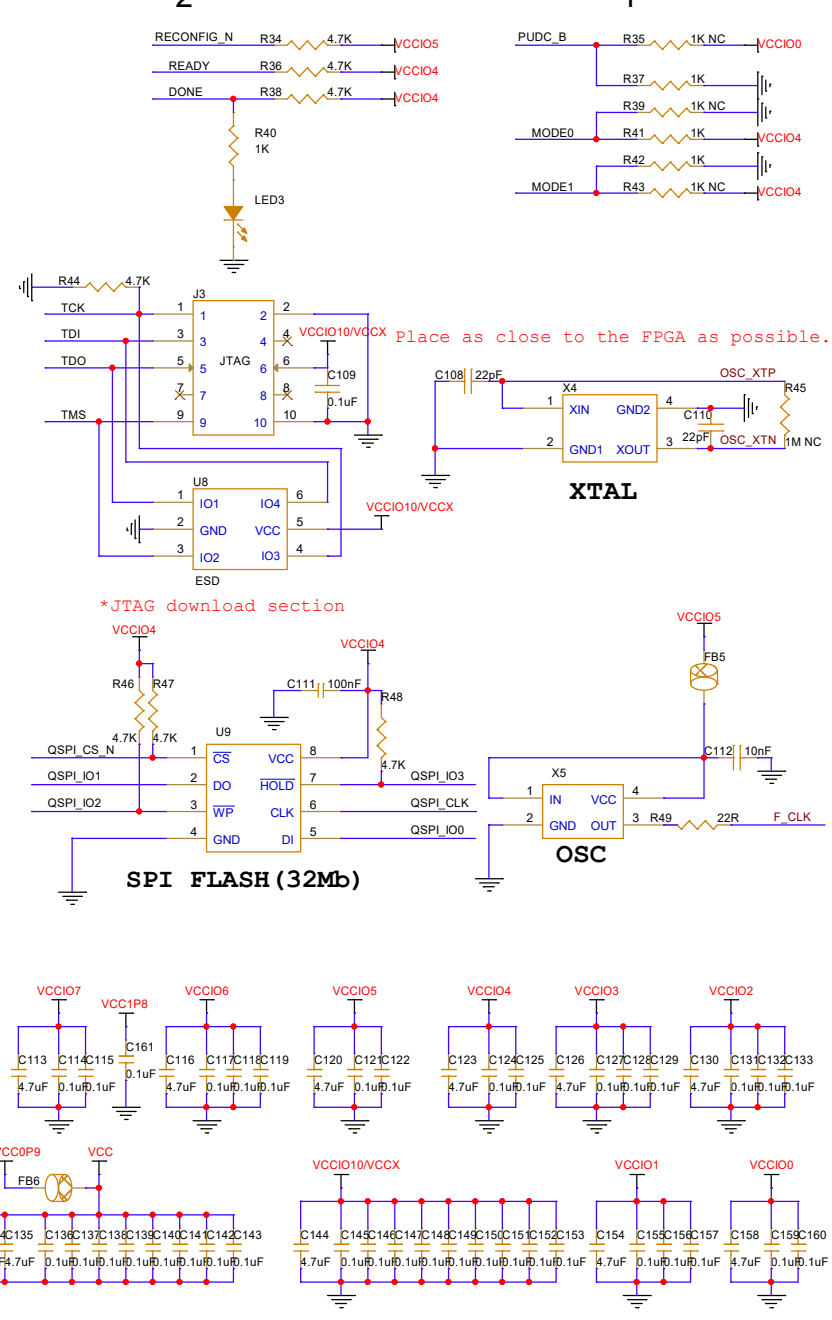
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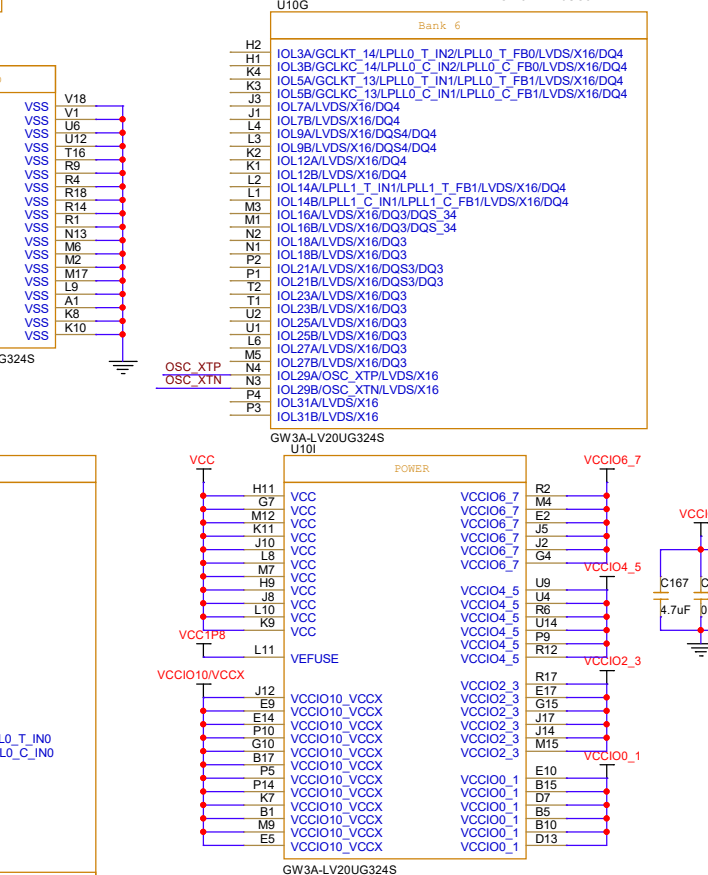
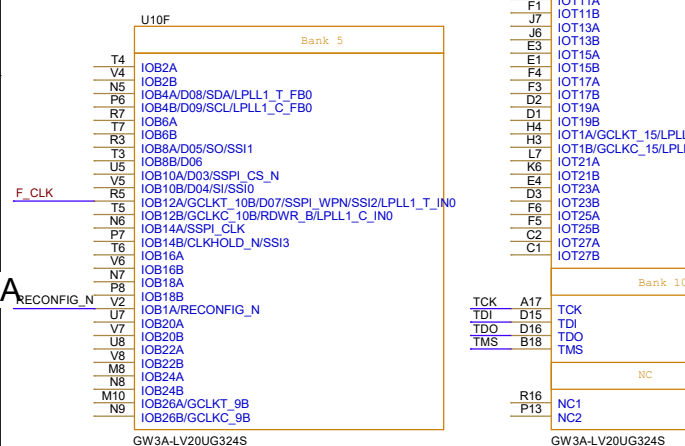
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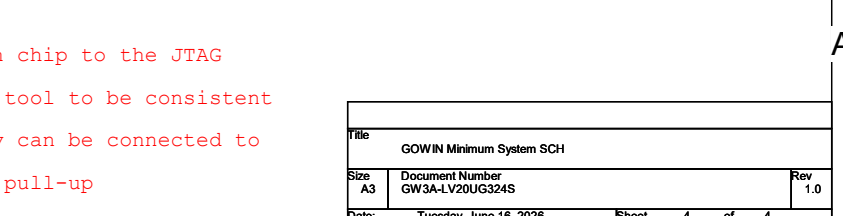
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